



VISHWAKARMA GOVERNMENT ENGINEERING COLLEGE, CHANDKHEDA

Brief Report on Webinar

Name of Department/Organizer	:	EC Engineering Department
Date & Time	:	25/6/2020 2 to 3 P.M
Venue	:	Online Google meet Platform
No of Participants	:	85

Objective of the Webinar:

- To help students to gain information regarding recent opportunities in VLSI industry.
- To provide information to students for different tools and design methodologies adopted by industry for VLSI system design.
- To aware the students about different career options in the field of VLSI Design.

Brief about event and outcome of event

- The webinar was started at 2:00 pm with welcome note by Prof. Alpesh Patel. He shared the brief details about the speaker with the participants. Mr. Nilesh Ranpura started the session with overview of e-infochip. He talked on experience, revenues, and products on which they are working. He explained semiconductor industry ecosystem, foundries, EDA / CAS Companies and IP vendors. Then he has explained electronics product design cycle in brief. He has nicely elaborated role / scope of VLSI in the Automotive industry. He has deeply talked about the working structures of VLSI industry with the role of software, system, silicon and mechanical design.
- He has also explained the feature trends of technology in the VLSI industry. He has talked the technology resolution in terms of challenges, size, manufacturing process, cost etc. starting from 90nm to 7 nm chip designs. He briefed the participants about the importance of Design for testability to improve yield. He briefed the advance networking switches and routers.

- He has nicely talk about importance of space ASIC, DFT and radiation harden design. At the end he has explain the need or skill requirement of the industry from the students and some research area. He has also motivated students to opt career in this stream.
- During question-answer session, many students and faculties had interacted with the expert and asked their questions through chat window. Mr Nilesh Ranpura had given all answers satisfactorily. The Expert shared his contact details also with the participants for any further query.

• Event Photographs

meet.google.com/msw-krbq-fzn?authuser=2

N Nilesh Ranpura is presenting

Webinar for Carrier in V

People (31)

You 2:01 PM
we wait for expert to join

Jayesh Diwan 2:07 PM
GOOD AFTERNOON SIR.

JAGRUTI NAIK 2:08 PM
Good afternoon sir

Ketan Patel 2:08 PM
Good afternoon sir

Jograjyia Kishan 2:08 PM
Good afternoon sir

Jayesh Diwan 2:09 PM
Yes sir, You are audible.

Ketan Patel 2:09 PM

Infochips Overview

21 YEARS Pure-play Product Engineering Services	60% BUSINESS from Fortune 500 clients	80% REVENUE Connected Devices & Cloud Enablement	500+ PRODUCTS 40+ million deployments across 130 countries	1500+ EMPLOYEES 3 Countries 10 Design Centers
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Aerospace Semiconductor Security, Surveillance & Access Control Consumer Electronics Consumer Software

The Solutions People

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N Nilesh Ranpura is presenting

NAKUM HASAM... and 26 more

2:42 PM

You

What Industry see Research Areas/Challenges in ESM at All Level ?

Device Physics Level :
14nm, 10nm, 7nm, Nano-Scale, MEMS, Sensors, Analog, FPGA and ASIC

Implementation Level :
(a) Integrated Software/CAD tools
(b) Optimization approach/customized tool/Modelling tools
(c) Methodologies : Low Power, Formal, Multi-Processing
(d) Larger, Complex, High feature, Reliable devices

Application Level :
(a) System design skills.!! Challenges are increasing, will keep increase and will foster growth of ESL

Nilesh Ranpura Payal Savani

Aplesh Patel navin ganeshan

Ketan Patel Rutvik patel