



## VISHWAKARMA GOVERNMENT ENGINEERING COLLEGE, CHANDKHEDA

### Brief Report on "Industry Visit at eInfochips Ltd., Ahmedabad"

Name of Department/Organizer	:	Electronics & Communication Department and IEI Student Chapter
Date & Time	:	2/04/2019
Venue	:	Ahmedabad
No of Participants	:	54

#### • Objective of the Event

- To help students to gain information regarding complete VLSI Design flow and recent industry trends in VLSI
- To provide exposure to students for different tools and design methodologies adopted by industry
- To understand different career opportunities in the field of VLSI.

#### • About eInfochips Ltd.:

- eInfochips (An Arrow Company) is a VLSI based product engineering and software R&D services company with over 20 years of experience, 500+ product developments, and over 40M deployments in 140 countries across the world. It was founded in the year 1994 by Mr. Pratul Shroff, CEO. It is having partnership with global market leaders like Microsoft, ARM, Qualcomm, Intel, cadence, Xilinx, etc.

#### • About Visit:

- Around 54 students of BE - 6<sup>th</sup> Semester and ME - 2<sup>nd</sup> Semester from EC department along with two faculties Prof. Kalpesh Chaudhary and Prof. Navin Ganeshan had visited the company on 2<sup>nd</sup> April, 2019.
- The first session of introduction began at 10:45 am. Students visited Experience Zone, an exhibition of different products designed by Einfochips Ltd. They demonstrated working products developed by einfochip in various domains such as VLSI chips, IoT, Cloud and connected devices, Aerospace, Security, Surveillance and access control, Home Automation and Health care
- Company had appointed three domain experts Mr. Ramesh Dewani, Mr. Desai and Ms. Khushali Dudhagara for this lecture session. Mr. Desai briefed about company profile, achievements, awards and their novel initiative to spread awareness among students about VLSI field. Mr. Ramesh Dewani explained complete VLSI design flow and different tools used by eInfochips for design, verification and testing of VLSI chips. He explained technology resolution in terms of challenges, size, manufacturing process, cost

etc. starting from 140 nm to 5 nm chip designs. Ms. Khushali Dudhagara was DFT (Design for Test) engineer. She mentioned the importance of testing before shipping the manufactured chips. She also mentioned different faults incurred while chip fabrication.

- The team of experts explained different job profiles and career opportunities in VLSI domain. They motivated students to opt career in this stream. They asked different questions to students related to previous sessions and felicitated students with token of appreciation for correct answers.
  - The session was concluded with vote of thanks by the team and by presenting a Memento to faculty.
  - Lunch was arranged for students and faculty by company.
- **Visit Photographs:**



