

**VISHWAKARMA GOVERNMENT ENGINEERING COLLEGE, CHANDKHEDA**  
POWER ELECTRONICS ENGINEERING DEPARTMENT

**CLASS : POWER ELECTRONICS ENGINEERING SEM 3**  
**TIME TABLE FOR ODD SEM 2018**  
(CODE): 3F

with effect from 16/06/2018

TIME	MONDAY		TUESDAY		WEDNESDAY		THURSDAY		FRIDAY		SATURDAY				
	SUBJECT	BATCH STAFF	ROOM	SUBJECT	BATCH STAFF	ROOM	SUBJECT	BATCH STAFF	ROOM	SUBJECT	BATCH STAFF	ROOM			
02 9:30 to 10:30				CAN LEC	DVG	G-106				DCM&T LEC	AMH	G-106			
1 10:30 to 11:30	EEM LEC		G-106	EEM LEC		G-106	PPE LEC	DVG	G-106	CAN LEC	DVG	G-106			
2 11:30 to 12:30	AEM LEC		G-106	AEM LEC		G-106	DCM&T LEC	AMH	G-106	PPE LEC	HSW	G-106			
3 13:00 to 14:00	CAN LEC	AMH	G-106	PPE LEC	DVG	G-106	EMMI LEC	APP	G-106	EMMI LEC	NDM	G-106			
4 14:00 to 15:00	DCM&T LEC	NDM	G-106	DCM&T LEC	NDM	G-106									
5 15:15 to 16:15	PPE LAB	3F1	G102A	CAN LAB	3F1	AMH	G-105	DCM&T LAB	3F1	AMH	G107A	DETA LAB	3F1	AMH	G102A
	DCM&T LAB	3F2	G-105	EMMI LAB	3F2	APP	G102B	PPE LAB	3F2	AMH	G107A	DETA LAB	3F2	TYR	G102B
6 16:15 to 17:15	EMMI LAB	3F3	G102B	DCM&T LAB	3F3	NDM	G107A	PPE LAB	3F3	SS	G102A	DETA LAB	3F3	JMP	G-105

INT	FACULTY NAME
VGP	Prof. V. G. Patel
NDM	Prof. N. D. Mehta
APP	Prof. A. P. Patel
HSW	Prof. H. S. Wani
DVG	Prof. D. V. Garasiya
AMH	Prof. A. M. Haque

- 3F POWER ELECTRONICS ENGINEERING SEM 3 CLASS ( ALL BATCH )
- 3F1 POWER ELECTRONICS ENGINEERING SEM 3 ( 1 TO 20 )
- 3F2 POWER ELECTRONICS ENGINEERING SEM 3 ( 21 TO 40 )
- 3F3 POWER ELECTRONICS ENGINEERING SEM 3 ( 41 TO 60 )
- 3F4 POWER ELECTRONICS ENGINEERING SEM 3 ( 61 ONWARDS )

*(Signature)*  
HOD, POWER ELECTRONICS ENGG DEPT.  
VISHWAKARMA GOVERNMENT ENGINEERING COLLEGE, CHANDKHEDA.

*(Signature)*  
**06-06-18**



VISHWAKARMA GOVERNMENT ENGINEERING COLLEGE, CHANDKHEDA

POWER ELECTRONICS ENGINEERING DEPARTMENT

TIME TABLE FOR ODD SEM 2018

CLASS : POWER ELECTRONICS ENGINEERING SEM 5

(CODE): 5F

with effect from 18/06/2018

TIME	MONDAY		TUESDAY		WEDNESDAY		THURSDAY		FRIDAY		SATURDAY	
	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM
02 9:30 to 10:30												
1 10:30 to 11:30	MCPE LAB MCPE LAB CSE LAB II LAB	5F1 HSW G102B 5F2 VGP G-104 5F3 DVG G-105 5F4 AMH G102A	PEPR-I LAB PEPR-I LAB II LAB CSE LAB	5F1 VGP G-105 5F2 SS G102A 5F3 NDM G102B 5F4 DVG G-104	II LAB CSE LAB MCPE LAB PEPR-I LAB	5F1 APP G102A 5F2 SS G102B 5F3 VGP G-104 5F4 G-105					DE-IIA LAB DE-IIA LAB DE-IIA LAB DE-IIA LAB	5F1 NDM G107A 5F2 SBP G107B 5F3 AAP G-104 5F4
2 11:30 to 12:30												
3 13:00 to 14:00	IE(DM) LEC	G-103	IE(DM) LEC	G-103			CSE LEC	VGP G-103	CSE LAB II LAB PEPR-I LAB PEPR-I LAB	5F1 SS G102A 5F2 APP G102B 5F3 HSW G107A 5F4 VGP G107B		
4 14:00 to 15:00	MCPE LEC	G-103	MCPE LEC	G-103			II LEC	AMH G-103				
5 15:15 to 16:15	PEC-I LEC	DVG G-103	CSE LEC	G-103	CSE LEC	VGP G-103	II LEC	NDM G-103	PEC-I LEC	APP G-103		
6 16:15 to 17:15	MCPE LEC	HSW G-103	PEC-I LEC	G-103	PEC-I LEC	DVG G-103	MCPE LEC	HSW G-103	PEC-I LEC	APP G-103		

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AMH	Prof. A. M. Haque

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HOD, POWER ELECTRONICS ENG66 DEPT.

VGS-HWAKARMA GOVERNMENT ENGINEERING COLLEGE, CHANDKHEDA

- SF POWER ELECTRONICS ENGINEERING SEM 5 CLASS (ALL BATCH)
- SF1 POWER ELECTRONICS ENGINEERING SEM 5 (1 TO 20)
- SF2 POWER ELECTRONICS ENGINEERING SEM 5 (21 TO 40)
- SF3 POWER ELECTRONICS ENGINEERING SEM 5 (41 TO 60)
- SF4 POWER ELECTRONICS ENGINEERING SEM 5 (61 ONWARDS)

*(Signature)*  
06-06-18



VISHWAKARMA GOVERNMENT ENGINEERING COLLEGE, CHANDKHEDA

POWER ELECTRONICS ENGINEERING DEPARTMENT

TIME TABLE FOR ODD SEM 2018

CLASS : POWER ELECTRONICS ENGINEERING SEM 7

(CODE) : 7F

with effect from 18/06/2018

TIME	MONDAY		TUESDAY		WEDNESDAY		THURSDAY		FRIDAY		SATURDAY	
	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM	SUBJECT	BATCH STAFF ROOM
02 9:30 to 10:30												
1 10:30 to 11:30	IA (DE) LEC	NDM G-103	IA (DE) LEC	NDM G-103	PED LEC	HSW G-103	IDC-II LEC	NDM G-103	DSP LEC	VGP G-103		
2 11:30 to 12:30	IA (DE) LEC	APP G-103	PED LEC	APP G-103	PED LEC	HSW G-103	DSP LEC	HSW G-103	IA (DE) LEC	APP G-103		
3 13:00 to 14:00	IDC-II LAB DSP LAB PED LAB IA (DE) LAB	7F1 7F2 7F3 7F4	G107B G-104 G-105 G102A	DSP LAB PROJ.-II LAB IA (DE) LAB IDC-II LAB	7F1 7F2 7F3 7F4	HSW G-104 G-105 G-105	IA (DE) LAB IDC-II LAB DSP LAB PED LAB	7F1 7F2 7F3 7F4	PROJ.-II LAB PED LAB PROJ.-II LAB PROJ.-II LAB	7F1 7F2 7F3 7F4	G-105 G-104 DVG G-104	
4 14:00 to 15:00												
5 15:15 to 16:15	IDC-II LEC	AMH G-106	APED & IC LEC	HSW G-106	DSP LEC	HSW G-106	APED & IC LEC	VGP G-106	PROJ.-II LAB IA (DE) LAB PROJ.-II LAB	7F1 7F2 7F3 7F4	G-106 G-104 G102B G107A	
6 16:15 to 17:15	IDC-II LEC	AMH G-106	APED & IC LEC	VGP G-106	IDC-II LEC	NDM G-106	PED LEC	APP G-106				

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HOD, POWER ELECTRONICS ENGG DEPT.  
VISHWAKARMA GOVERNMENT ENGINEERING COLLEGE, CHANDKHEDA.

- 7F POWER ELECTRONICS ENGINEERING SEM 7 CLASS (ALL BATCH)
- 7F1 POWER ELECTRONICS ENGINEERING SEM 7 ( 1 TO 20)
- 7F2 POWER ELECTRONICS ENGINEERING SEM 7 ( 21 TO 40)
- 7F3 POWER ELECTRONICS ENGINEERING SEM 7 ( 41 TO 60)
- 7F4 POWER ELECTRONICS ENGINEERING SEM 7 ( 61 ONWARDS)

06-06-18